



⑪ Publication number:

**0 451 909 A2**

12

**EUROPEAN PATENT APPLICATION**

②<sup>1</sup> Application number: 91200799.4

⑤ Int. Cl.<sup>5</sup>: **H03F 1/32**

② Date of filing: 05.04.91

③ Priority: 09.04.90 IT 1997590

④ Date of publication of application:  
**16.10.91 Bulletin 91/42**

⑧<sup>84</sup> Designated Contracting States:  
CH DE ES FR GB GR IT LI SE

71) Applicant: **SIEMENS TELECOMUNICAZIONI S.P.A.**  
**SS. 11 Padana Superiore Km. 158**  
**I-20060 Cassina de Pecchi (Milano)(IT)**

⑦2 Inventor: **Abbiati, Antonio**

**Via Cap. G. Antoninetti, 62  
I-20079 S. Angelo Lodigiano (Milano)(IT)**

Inventor: **Buoli, Carlo**

**Via di Dietro, 21**

**I-41030 Mirandola (Modena)(IT)**

Inventor: **Cervi, Luigi**

**Via M. Buonarroti, 5**

**I-26010 Olmeneta (Cremona)(IT)**

74 Representative: Mittler, Enrico et al  
c/o Marchi & Mittler s.r.l. Viale Lombardia, 20  
I-20131 Milano(IT)

⑤4 Predistortion linearizer for microwave power amplifiers.

57) Predistortion linearizer for microwave power amplifiers wherein a single transistor (FT1), e.g. the GaAsFET type, subpolarized near the pinch-off condition, carries out the functions both of gain ex-

pander amplifier for recovery of amplitude distortion of the power amplifier and command signal generator for a dephaser element (D1) for recovery of the phase distortion of the power amplifier (FIG. 1).

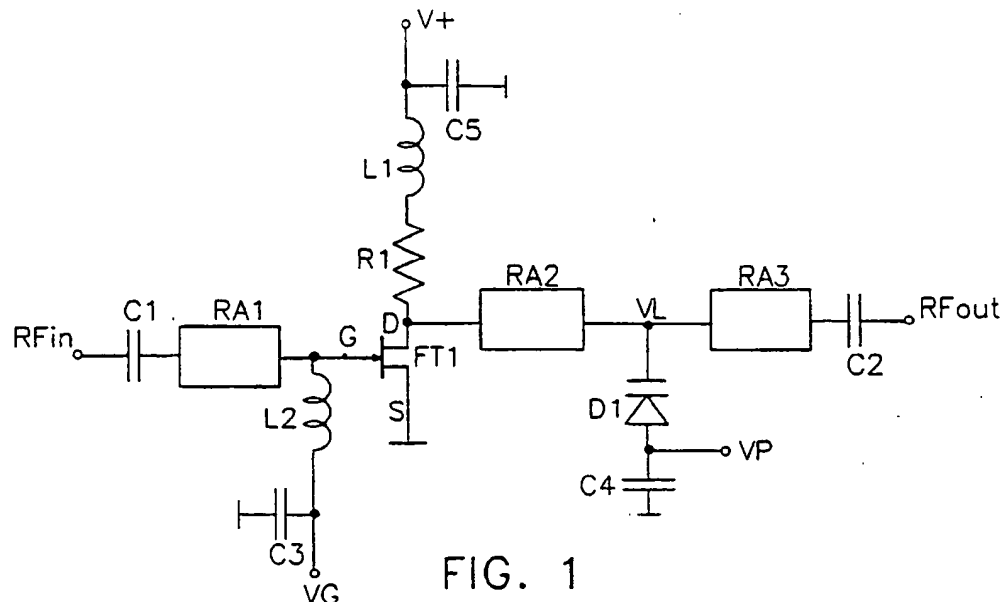


FIG. 1

The present invention relates to improvements in microwave power amplifiers.

Modulations presently in use in radio links, essentially Quadrature Amplitude Modulation (QAM), impose very stringent linearity requirements for the radiofrequency power amplifier of the transmitter on which depends not a little the degradation of the modulated signal.

The power output from the final amplifier devices must be considerably lower than their saturation power so that the nonlinear distortions thereof introduced satisfy the specifications of the transmitter. Said distortions are due to compression of gain at high power found in the trend of the Amplitude Modulation (AM/AM) distortion curve at high power and the amplitude/modulation phase modulation (AM/PM) conversion curve, again at high power.

Obviating these distortions usually involves oversizing said final amplifiers and hence high cost of the power amplifying section.

As known in itself use of a linearizing network in the transmitting section permits use of power devices with lower saturation for given distortion produced with a resulting increase of efficiency, e.g. for application in the transmitters of on-board repeaters in satellite communication systems or, for a given saturation power of final devices, allows higher linearity of the amplifier, e.g. for applications in transmitters for earth stations in said satellite communication systems.

A linearization technique presently well known is termed 'feed forward error control' and includes all the linearizers which use an auxiliary microwave amplifier which amplifies an error signal obtained by determining the difference between the input signal and the distorted one appropriately attenuated output from the main amplifier. The error signal is proportional to the distortions generated by the main amplifier so that, again added with appropriate phase and amplitude at the output of the main amplifier, it reduces the distortions affecting the output signal.

It is clear that the merit figure or degree of quality of this linearization system depends almost exclusively on the balancing of the final adder or coupler, which subtracts the error signal from the output signal of the amplifier. A balancing regulation circuitry (in amplitude and phase) of said coupler is therefore necessary and is quite complex. In addition it is a true amplifier-linearizer complex in itself, not an addition to improve a known amplifier.

Another present linearization technique calls for the use of RF (radio frequency) predistorters, i.e. nonlinear networks inserted upstream of the final microwave amplifier, which distort the input signal by means of networks embodied with components which work in nonlinear state in order to compensate for the AM/AM distortion curve and the

amplitude/phase conversion curve AM/PM of the final power amplifier and guaranteeing better linearity of the transmitting section. The main drawback of said known predistorters consists however of the excessive complexity of said networks and thus of their still excessive cost.

An example of predistortion linearizer for microwave power amplifiers is described in Italian patent application no. 19497-A/87 filed by the same applicant 26 February 1987.

In said previous Italian patent application there is described a predistortion linearizer applicable upstream of the power amplifier characterized in that it comprises a main network including a phase modulator and an amplitude modulator arranged in cascade and a secondary network with base band frequency including means for detecting in amplitude and filtering part of the input signal so as to produce a detected signal being a function of the instantaneous input power and a pair of adjustable gain amplifiers supplied with said detected signal and acting on said modulators in such a manner as to give them nonlinear response curves such as to compensate independently for both amplitude and phase nonlinearity of the power amplifier.

Said linearizer, although it has all the advantages compared with the known art listed in the application, is still of costly and cumbersome embodiment, principally because of the presence therein of derived branches.

Accordingly the object of the present invention is to overcome the above drawbacks and indicate a predistortion linearizer for microwave power amplifiers extremely simplified in its circuitual structure. Indeed it comprises essentially a single transistor which carries out the functions both of the gain expander amplifier for recovery of the amplitude distortion of the power amplifier and generator of a command signal for a phase shift element for recovery of the phase distortion of the power amplifier. In a first form of embodiment said single transistor is placed upstream of the final power amplifier and followed by the phase shift element. In a second form of embodiment its function is fulfilled by the same final power amplifier.

To achieve said purposes the object of the present invention is a predistortion linearizer for microwave power amplifiers as described in claim 1.

The variant embodiments described in the dependent claims comprise a further object of the present invention.

Further objects and advantages of the present invention will be made clear by the following detailed description of an embodiment thereof and the annexed drawings given for purely nonlimiting explanatory purposes and wherein -

FIG. 1 shows the circuit diagram of a first exam-

ple of realization of the linearizer which is the object of the present invention, FIGS. 2, 3, 4 and 5 show the trends of some characteristic parameters of the linearizer and final power amplifier as a function of the power  $P_i$  of the input signal,

FIG. 6 shows a first embodiment variant of the phase shift element D1 of FIG. 1, and

FIG. 7 shows a second embodiment variant of said phase shift element.

FIG. 1 shows a possible form of circuit embodiment of the linearizer which is the object of the invention and is applied upstream of a power amplifier for the purpose of compensating for the amplitude and phase distortions. Indeed the amplitude and phase response curves of a microwave power amplifier are typically as shown in FIG. 2 wherein is shown the qualitative shape of the output power  $P_u$  (AM/AM distortion curve) and the input-output phase variation  $V_F$  (AM/PM conversion curve) as a function of input power  $P_i$ . The linearizer has thus a dual purpose, i.e. first to expand its own gain at the  $P_i$  values so that in FIG. 2 the knee of the curve  $P_u$  occurs to compensate for said knee and continue the linear shape of  $P_u$  even in the immediate proximity of saturation, this latter zone being indicated by a horizontal shape which however cannot be offset; the second purpose is to vary its own input-output phase in a manner opposite to the shape of  $V_F$  in FIG. 2.

In FIG. 1  $R_{Fin}$ ,  $R_{Fout}$  indicate the input and output connectors respectively of a radiofrequency signal.

C1, C2 ...C5 indicate capacitances and L1, L2 inductances of known type.

FT1 indicates a GaAsFET transistor (with gallium arsenide field effect) equipped as known with three connectors, source S, drain D and gate G1, used in a common source configuration.

R1 indicates a polarization resistance of FT1.

D1 indicates a varactor diode.

RA1, RA2, RA3 indicate common impedance matching networks for input, interstage and output respectively embodied for example in microstrip.

VG, VP and  $V+$  indicate the supply voltages of the gate of FT1, the anode of D1 and fixed positive respectively.

C1 and C2 are used as block capacitances of the continuous component at the input  $R_{Fin}$  and the output  $R_{Fout}$  respectively. C4 and the networks C3-L2, C5-L1 form low-pass decoupling filters between signal and supplies VG, VP and  $V+$ ; C3, C4 and C5 have a grounded end.

The input signal is applied to the gate of FT1 through C1 and RA1; the voltage  $V_g$  is also applied to the gate of FT1 through the filter C3-L2. The source of FT1 is connected to ground while the voltage  $V+$  is brought to the drain through the filter

C5-L1 and the resistance R1. The cathode of the diode D1 is connected also continuously to the drain of FT1 through the network RA2 and to the output  $R_{Fout}$  through the network RA3 and C2.

The transistor FT1 amplifies the RF signal applied at the input  $R_{Fin}$ ; the values of fixed supply voltage  $V+$ , resistance R1 and voltage VG are chosen in such a manner as to keep FT1 in under-polarization conditions, i.e. with the working point near the pinch-off region (low values of drain-source current  $I_{ds}$  and gate-source voltage  $V_{gs}$ ).

In this manner an increase in the power of the RF input signal is capable of changing the working point of the device, i.e. increasing the continuous component of the current  $I_{ds}$ . Since the gain of FT1 depends on said continuous component of  $I_{ds}$ , the increase of the latter causes an increase in gain and supplies the desired expansion effect.

FIG. 3 shows the qualitative shape of the gain curve  $G_i$  obtained as a function of the power  $P_i$  of the signal RF at the input  $R_{Fin}$ . By varying the parameters of the polarization network of FT1, i.e. VG, R1 and  $V+$ , it is possible to obtain a shape of  $G_i$  optimized from the point of view of compensation of the AM/AM distortion curve. The figures also show as a function of  $P_i$  the corresponding qualitative shape of the continuous component of  $I_{ds}$ .

Due to the presence of the resistance R1, as the continuous component of  $I_{ds}$  increases the continuous component of the drain-source voltage  $V_{ds}$  of FT1 decreases and hence that of the polarization voltage  $V_L$  of the varactor diode D1 (indeed D1 is connected continuously to the drain of FT1).

Thus a detected voltage of the modulating signal proportional to the continuous component of  $I_{ds}$  is localized and used as a control signal of the phase variation introduced by the downstream device which in the nonlimiting example described is the varactor diode D1.

The effect obtained is thus a modulation of said voltage FL which, as is known, influences the internal capacitance value of the varactor and hence the phase shift introduced therefrom onto the RF signal. This effect can be used with advantage to offset the conversion curve AM/PM.

FIG. 4 shows the qualitative shape of the phase variation FL as a function of the power  $P_i$  of the input signal. The shape of FL can be made specular in relation to that of  $V_F$  (shown with broken line in FIG. 4) by appropriate sizing of the parameters which influence it, i.e. R1 and VP.

To sum up, the transistor FT1 fulfils a dual function: that of gain expander to offset the AM/AM distortion curve and that of detector of a modulation voltage which directly pilots a phase shifter to offset the AM/PM conversion curve.

More specifically, the gain expansion charac-

teristic is adjusted by varying  $V_G$  and the phase characteristic is adjusted by varying  $V_P$ .

The device of FIG. 1 follows very rapidly the dynamics of the input signal RF, i.e. its response as gain expander and phase shifter is a wide band for the modulating signal because it consists of a single RF branch. This is in effect very important because it makes the device usable even when the multicarrier modulating signal is wide band, e.g. in transponders for satellites.

In addition there is an undoubted advantage in terms of size and cost reduction of the components of the linearizer which can be embodied with microstrip or Microwave Integrated Circuit (MIC) technology using both discrete packaged components and chip-and-wire or integrated in Monolithic MIC (MMIC) technology.

Numerous variants on the embodiment described as an example in FIG. 1 are possible without going beyond the scope of the innovative principles contained in the inventive idea.

The linearization function can be performed by said final power stage consisting for example of a transistor of the FT1 type of FIG.1 brought to a similar working condition.

This may be explained by a digital example also with reference to FIG. 5 which shows the shapes of the output power  $P_u$  of the final power stage in two working conditions, normal ( $I_{ds}=2A$ ) and modified in accordance with the invention ( $I_{ds}=0,7A$ ) respectively, and the shape of the continuous component  $I_{ds}$  of the drain current of the transistor of said final stage, as a function of the input power  $P_i$ .

Assume a final power stage consisting of a GaAsFET transistor which in linear conditions gains 10dB with working point  $F_{ds}=10V$ ,  $I_{ds}=2A$  and output saturation power  $P_{usat}=40dBm$  at modulated signal frequencies of approximately 6-7GHz. Under these conditions the shape of output power  $P_u$  as a function of input power  $P_i$  is as indicated in FIG. 5 by parameter  $I_{ds}=2A$  with the knee zone to be linearized.

If we now decrease the gate-source voltage  $V_{gs}$  of the transistor to obtain  $I_{ds}=0,7A$  the gain of the linear zone decreases to approximately 7dB. Under these conditions when the input power  $P_i$  increases  $I_{ds}$  also tends to increase to a nominal value of 2A in conformity with the shape of  $I_{ds}$  shown in the figure. The increase of  $I_{ds}$  causes an increase in the gain  $G_i$  of the transistor just in the zone where the curve  $P_u(I_{ds}=2A)$  had the knee, introducing in this case a self-linearization effect: the amplitude in the knee zone of the curve  $P_u(I_{ds}=0,7A)$  decreases greatly, offsetting the distortion curve AM/AM.

As concerns offsetting the conversion curve AM/PM the phase variations can be offset by a

circuit of known type upstream piloted by the voltage  $V_{ds}$  of the final transistor as described with reference to FIG. 1.

The variant now described can be used to advantage in QAM multi-level modulation systems where the RF output power is not constant but varies considerably in relation to the average value depending on the point of the constellation of symbols to be transmitted; for example in the 64QAM system there is a difference of approximately 8dB between average and maximum RF output power. There is a considerable current savings; for the low levels of the constellation for which the output power  $P_u$  is low the current  $I_{ds}$  stays around 0,7A and reaches 2A only for the higher levels for which  $P_u$  is high. Since the points of the constellation are all equally probable, the average  $I_{ds}$  current will be approximately 1.1-1.2A, and not 2A, with a clear consumption savings. Decrease of gain in the linear zone from 10 to 7dB does not involve problems because the 3dB difference is necessary only at the power peaks.

A second variant calls for the embodiment of the phase shifter as in FIG. 6 wherein the same symbols as in FIG. 1 indicate the same components interconnected in the same manner.

In FIG. 6 there has been added a second varicap diode D2 equal and in antiparallel to D1 for the signals. The anode of D2 is connected to the cathode of D1 while the cathode of D2 is polarized by the direct voltage  $V_{P2}$  and is connected to ground for the signals through the filter capacitance C6.

The embodiment of FIG. 6 serves in case it is desired to offset a phase variation which may be either leading or delaying. One diode offsets the delaying phase variations and the other the leading ones; by appropriately sizing the voltages  $V_P$  and  $V_{P2}$  it is possible to make one of the two diodes work while excluding the other, thus obtaining a leading phase shift with D2 inserted or delaying with D1 inserted.

A third variant calls for embodiment of the phase shifter as in FIG. 7 wherein the same symbols as in FIG. 1 indicate the same components interconnected in the same manner and performing the same functions.

In FIG. 7 the phase shifter is embodied by a GaAsFET transistor indicated by FT2 and polarized in such a manner as to function as a normal amplifier in a common source configuration with the continuous component of the drain-source current  $I_{ds2}=\frac{1}{2}I_{dss}$ . The latter is the maximum value of the drain current for  $V_{gs}=0$ .

R2 indicates the load resistance of FT2.

C7, C8 and C9 indicate capacitances and L3, L4 indicate inductances of known type. C9 and the networks C7-L3, C8-L4 form low-pass decoupling

filters between signals and supplies for output RFout, gate FT2 and drain FT2; C7, C8 and C9 have one end grounded.

RA4 indicates an output matching network with the same function as RA1, RA2 and RA3.

By varying the continuous component of the gate-source voltage of FT2 there is obtained a variation of the continuous component of the drain current  $I_{ds}$  which does not cause appreciable variation in the gain of FT2 since FT2 is in linear operation conditions, but the value of gate-source capacitance  $C_{gs}$  of FT2. The effect obtained is equivalent to that supplied by the varactor diode D1 of FIG. 1, i.e. variation of the phase FL of the output signal RFout which offsets the AM/PM conversion curve (see FIG. 4).

In this case also the voltage detected in the modulation of the modulating signal (which is proportional to the continuous component of  $I_{ds}$  of FT1 located at the ends of R1) is used as the control signal of the phase variation introduced by FT2. To bring said control signal to FT2 it is necessary to pair continuously the two transistors FT1 and FT2, bringing to the input of FT2 the voltage taken from R1 either in phase or phase inverted depending on whether the phase variation desired is leading or delaying respectively.

To introduce a phase delay there can be used the component indicated by AMP in FIG. 7, which is a continuous inverting amplifier with variable gain and a passing band having a width at least double that of the modulating input signal band. AMP receives and amplifies the continuous component of the drain voltage of FT1 at one end of R1 and supplies it with changed sign to the gate of FT2 through the filter C7-L3 to obtain a modulation effect of the continuous component of the gate voltage of FT2. This effect is translated into a variation of the capacitance  $C_{gs}$  of FT2.

AMP can be embodied by a reversing stage of any known type. If embodied by a GaAsFET transistor the entire circuit of FIG. 7 can be integrated in MMIC technology of simple and economical embodiment.

If FT2 is not to lead but delay in phase, the amplifier AMP will have the same characteristics as above except that it will not be reversing.

As another variant, the transistors called for in the various forms of embodiment described can also be of another type, e.g. bipolar.

## Claims

1. Predistortion linearizer for a final microwave power amplifier subject to gain compression and phase distortion characterized in that it comprises:
  - a first subpolarized transistor (FT1) which

constitutes a gain expander stage with increase in the power of the signal at its input; and

- a phase shifter (D1; D1, D2; FT2) controlled by the continuous component of the voltage present at the output of said first transistor, which introduces a phase distortion which offsets that of said final power amplifier.

2. Linearizer as in claim 1 characterized in that said first transistor (FT1) is placed upstream of said final power amplifier.
3. Linearizer as in claim 1 characterized in that said first transistor (FT1) is said final power amplifier.
4. Linearizer as in claim 2 or 3 characterized in that said first transistor (FT1) is a GaAsFET in common source configuration whose gate polarization voltage (VG) is adjustable to obtain said subpolarization.
5. Linearizer as in claim 2 or 3 characterized in that said first transistor (FT1) is bipolar in common emitter configuration whose base polarization voltage (VG) is adjustable to obtain said subpolarization.
6. Linearizer as in claim 2 characterized in that said phase shifter consists of a varactor diode (D1) placed in parallel for the signals and coupled continuously to the output of said first transistor (FT1), the polarization voltage (VP) of said diode being adjustable.
7. Linearizer as in claim 2 characterized in that said phase shifter consists of two varactor diodes (D1, D2) placed in antiparallel for the signals and coupled continuously at the output of said first transistor (FT1), the polarization voltage (VP, VP2) of each of said diodes being independently adjustable.
8. Linearizer as in claim 2 characterized in that said phase shifter is an amplifier stage placed downstream of said gain expander stage consisting of a second GaAsFET transistor (FT2) in common source configuration whose input is coupled continuously with the output of said first transistor (FT1).
9. Linearizer as in claim 2 characterized in that said phase shifter is an amplifier stage placed downstream of said gain expander stage consisting of a third bipolar transistor (FT2) in common emitter configuration whose input is

coupled continuously with the output of said first transistor (FT1).

10. Linearizer as in claims 8 or 9 characterized in that said continuous coupling between the first and second transistors is embodied by a wide band inverting continuous amplifier (AMP). 5
11. Linearizer as in claims 8 or 9 characterized in that said continuous coupling between the first and second transistors is embodied by a wide band noninverting continuous amplifier (AMP). 10

15

20

25

30

35

40

45

50

55

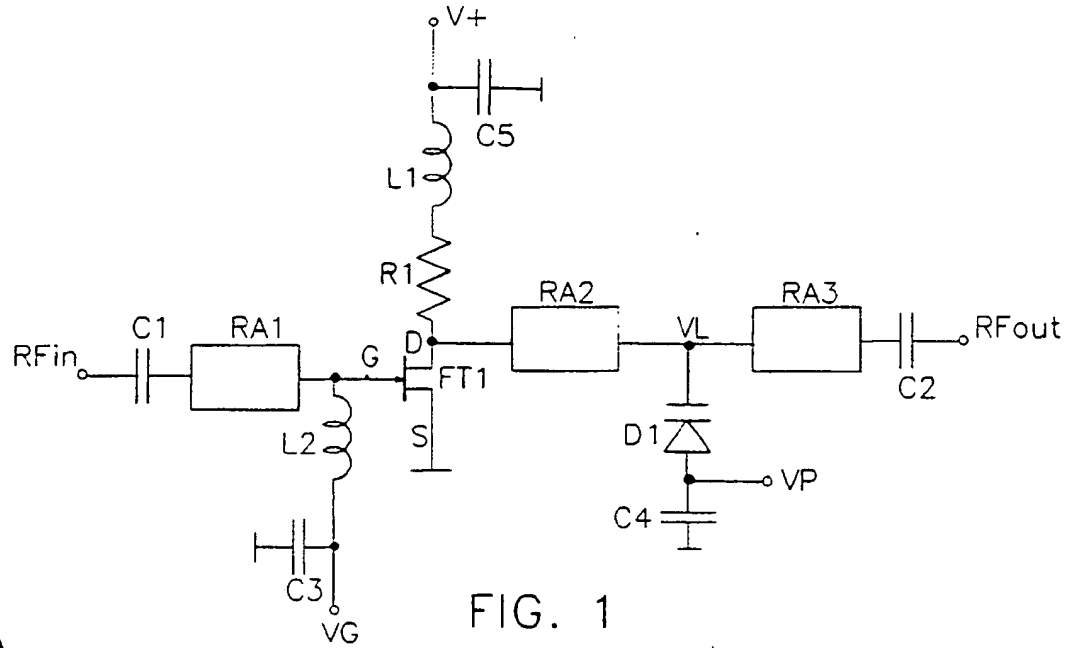


FIG. 1

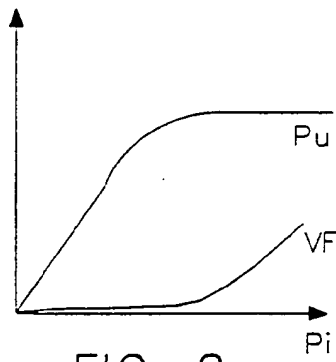


FIG. 2

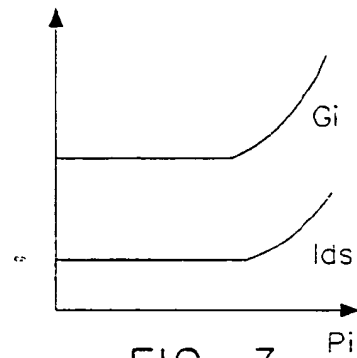


FIG. 3

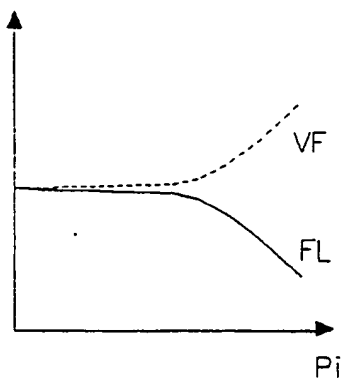


FIG. 4

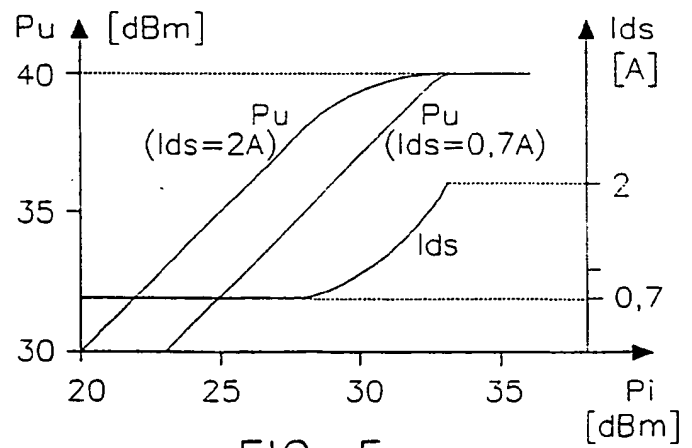


FIG. 5

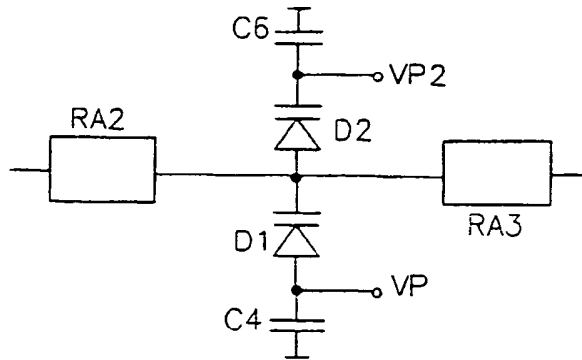


FIG. 6

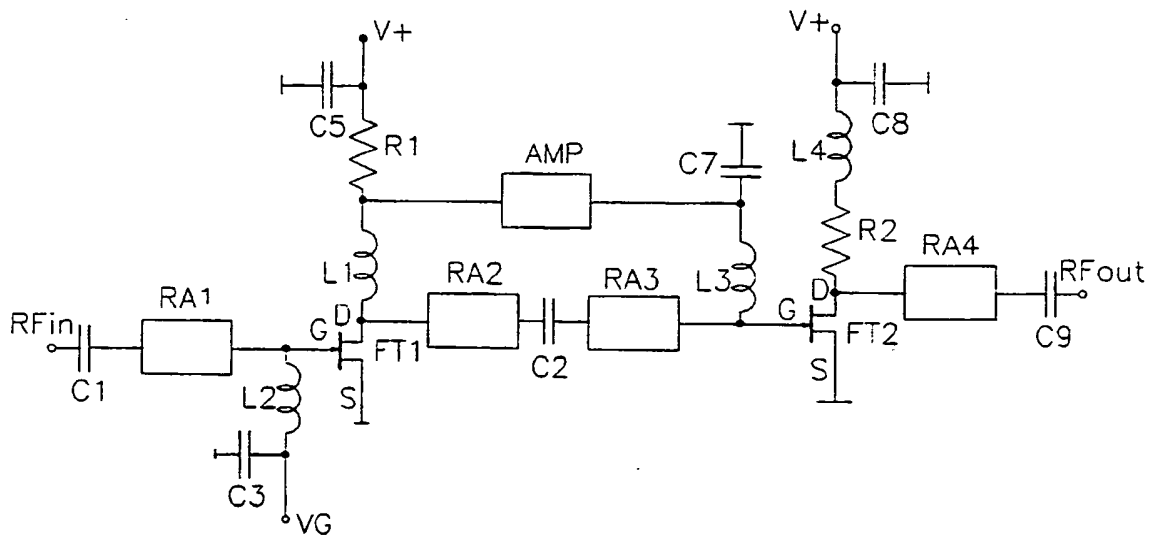


FIG. 7





Europäisches Patentamt  
European Patent Office  
Office européen des brevets



Publication number:

**0 451 909 A3**

12

## EUROPEAN PATENT APPLICATION

21 Application number: **91200799.4**

51 Int. Cl.<sup>5</sup>: **H03F 1/32**

22 Date of filing: **05.04.91**

30 Priority: **09.04.90 IT 1997590**

43 Date of publication of application:  
**16.10.91 Bulletin 91/42**

84 Designated Contracting States:  
**CH DE ES FR GB GR IT LI SE**

88 Date of deferred publication of the search report:  
**02.09.92 Bulletin 92/36**

71 Applicant: **SIEMENS TELECOMUNICAZIONI S.P.A.**  
**SS. 11 Padana Superiore Km. 158**  
**I-20060 Cassina de Pecchi (Milano)(IT)**

72 Inventor: **Abbiati, Antonio**  
**Via Cap. G. Antoninetti, 62**  
**I-20079 S. Angelo Lodigiano (Milano)(IT)**  
Inventor: **Buoli, Carlo**  
**Via di Dietro, 21**  
**I-41030 Mirandola (Modena)(IT)**  
Inventor: **Cervi, Luigi**  
**Via M. Buonarroti, 5**  
**I-26010 Olmeneta (Cremona)(IT)**

74 Representative: **Mittler, Enrico et al**  
**c/o Marchi & Mittler s.r.l. Viale Lombardia, 20**  
**I-20131 Milano(IT)**

54 **Predistortion linearizer for microwave power amplifiers.**

57 Predistortion linearizer for microwave power amplifiers wherein a single transistor (FT1), e.g. the GaAsFET type, subpolarized near the pinch-off condition, carries out the functions both of gain ex-

pander amplifier for recovery of amplitude distortion of the power amplifier and command signal generator for a dephaser element (D1) for recovery of the phase distortion of the power amplifier (FIG. 1).

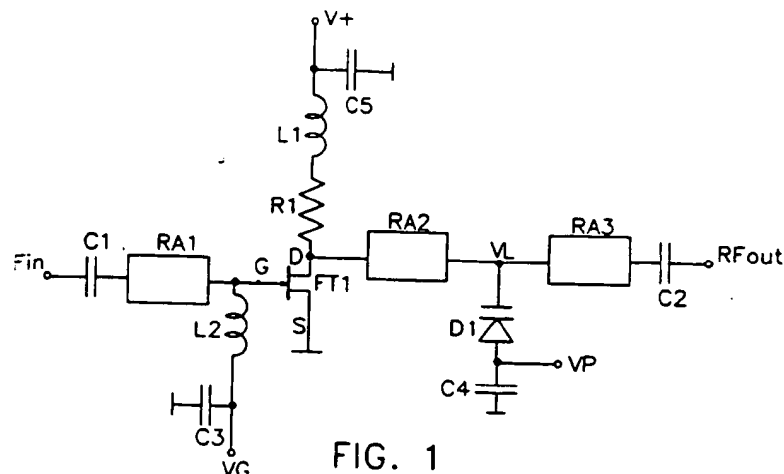


FIG. 1

EP 0 451 909 A3



European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number

EP 91 20 0799

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	US-A-4 532 477 (D.R. GREEN JR) * the whole document * ---	1-5, 8, 9	H03F1/32
A	GB-A-1 039 830 (STANDARD TELEPHONES AND CABLES LTD) * the whole document * ---	1, 6	
A	PATENT ABSTRACTS OF JAPAN vol. 2, no. 120 (E-78)(6900) 6 October 1978 & JP-A-53 085 142 ( NIPPON DENSHIN DENMA KOSHA ) 27 July 1978 * abstract * ---	1, 2	
A	US-A-4 465 980 (HO-CHUNG HUANG) * the whole document * ---	1, 2, 5	
A	CA-A-981 762 (J.J. SPISAR) * page 16, line 8 - page 18, line 28; figures 6, 8 * ---	1, 2, 4, 5	
A	DE-A-2 140 851 (ROHDE & SCHWARZ) * page 2, line 1 - line 33; figures * -----	1, 7	TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H03F
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 10 JULY 1992	Examiner TYBERGHIE G. M.
<b>CATEGORY OF CITED DOCUMENTS</b> X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ----- & : member of the same patent family, corresponding document			